

# **Forum Area**

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Nanoelec is a consortium of 22 organizations, a trusted circle within which industrial and academic partners work on technological research projects. The institute contributes to the Leti Innovation Days program, an event that rallies players from across the microelectronics industry. We are delighted to showcase a selection of industrial innovation success stories.



**Hughes Metras** Director of the Nanoelec **Technological Research** Institute



## DIGIGRAM

Tuesday, June 27

### 01:00 p.m.

IC industry: driving innovation and sustainability in wet surface preparation processes

### clijink ov terradona

TECHNIC

01:15 p.m. Cliiink<sup>®</sup> by Terradona is the first sorting habits reward program in France









#### Wednesday June, 28



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safehear

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12:00 p.m. Kalray DPU: An innovative processor architecture for AI at the edge

## 12:15 p.m.

ASCENT+ Access program to European infrastructures for nanoelectronics

#### 12:30 p.m.

The augmented ear: an innovation for industry and many other applications

### 12:45 p.m.

Maximize energy efficiency while minimizing CO2 impact with CEA's energy optimization tool

#### 01:00 p.m.

SiPearl is building the European low-power HPC dedicated microprocessor for exascale

#### 01:15 p.m.

Discover why international companies choose Grenoble Alpes

01:30 p.m. Wireless and batteryless sensing

01:45 p.m. 3D Free shape Printed OPV panels for self-powered IoT systems

#### Thursday, June 29



12:00 p.m. Game changer in 3D sensing with highly compact Si photonics FMCW LiDARs



ELICHENS

#### 12:15 p.m. Detecting and weighing drops of welfare in a concentrate of technology



12:30 p.m. Chainless pedaling device for active, automotive grade mobility

#### 12:45 p.m.

Air Quality Solutions: smart gas sensors and stations

#### 01:00 p.m. worms

Design and fabrication of integrable vibration sensors for predictive maintenance

### 01:15 p.m. ISRA

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injectpower<sup>\*</sup>

Aniah

Zero Electrical Error on 1" Silicon Spin •

Fab-as-a-service: prototyping & manufacturing of flexible printed electronics

01:30 p.m.



#### 01:45 p.m.

New algorithm for transistor-level, electrically accurate full chip analysis

















**JSYGN** 

DRACULA